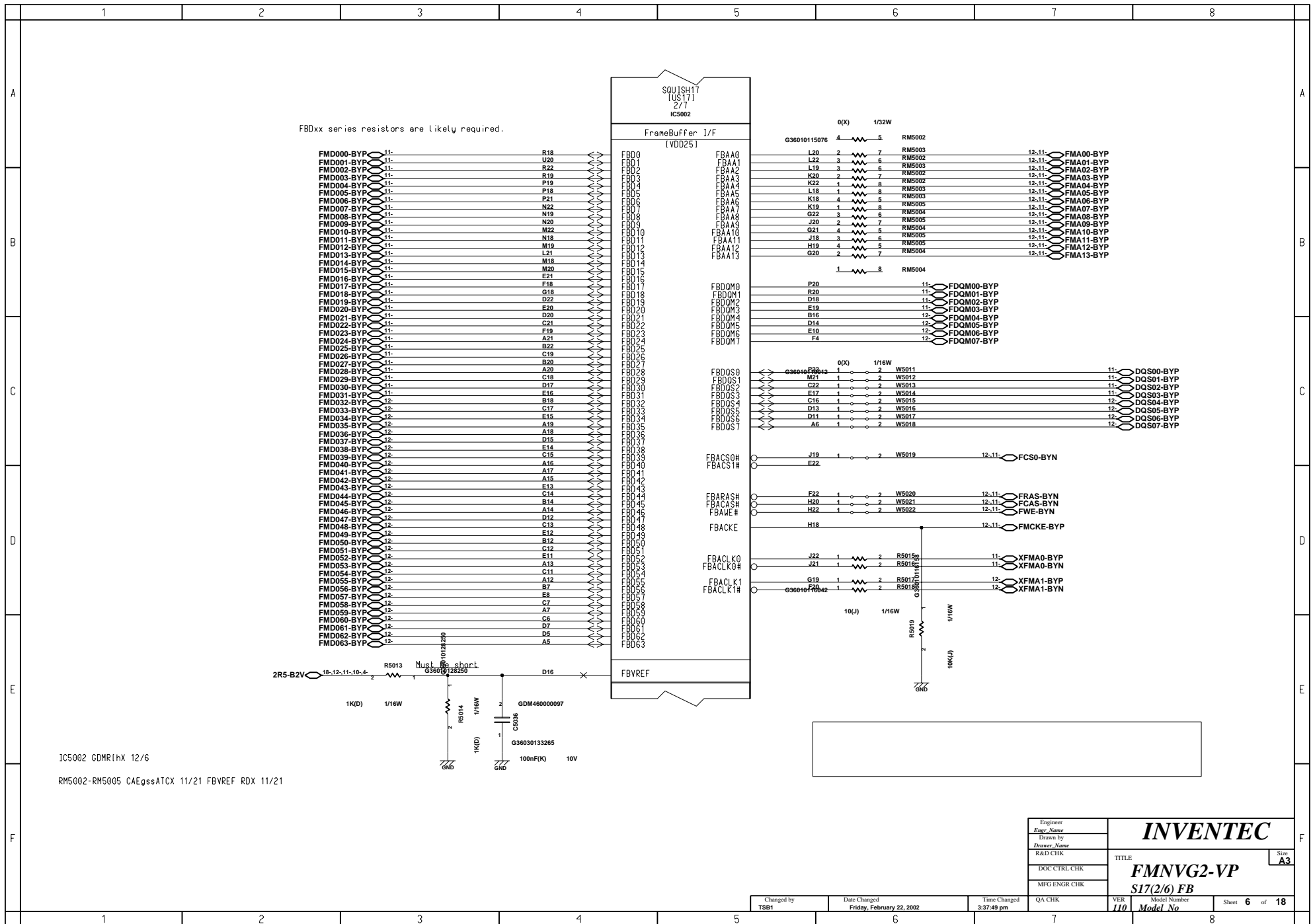
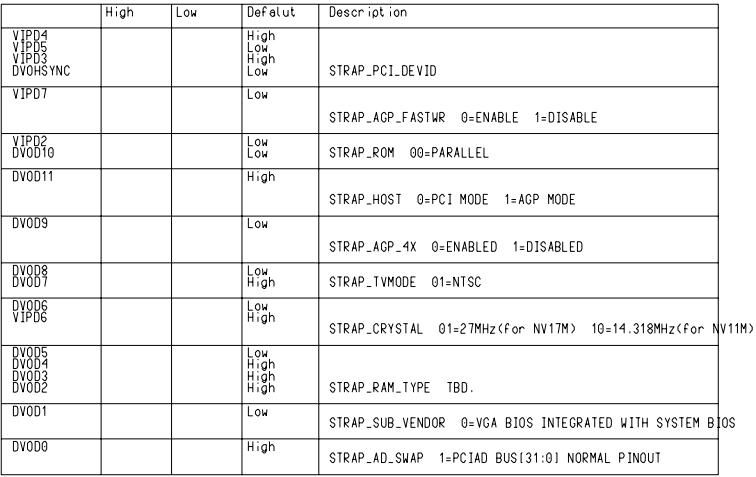


PCB	Gr	VGA_ID (1,0)	VGA	VRAM(CLOCK)	MEM_V
		0 0			
		0 1			
		1 0			
FMNVG*	TBD	1 1	NV17	DDR-16M 220MHz?	2.5V

{Vlg 11/17

Engineer	<div>INVENTEC</div>				<div>Size A3</div>	
Expr. Name						
Drawn by						
Drawer Name						
R&D CHK	TITLE	<div>FMNVG2-VP</div> <div>VGA I/F MISC</div>				
DOC CTRL CHK						
MFG ENGR CHK						
QA CHK	VER	Model Number	Sheet	4	of	18
	110	Model No.				





Engineer <i>Engr. Name</i>	<div style="text-align: center;"> <h1>INVENTEC</h1> </div>		
Drawn by <i>Dwgsr. Name</i>			
R&D CHK	TITLE	Size A3	
DOC CTRL CHK	<div style="text-align: center;"> <h2>FMNVG2-VP</h2> <h3>S17(3/6) MISC</h3> </div>		
MFG ENGR CHK	VER	Model Number	Sheet 7 of 18
QA CHK	110	<i>Model No</i>	

